

U.S. PTO
12/03/01

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APP. NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10003312	12/03/2001	327		2816	

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CONTINUING DATA VERIFIED
THIS APPLICATION IS A CONTINUATION OF 02/505,004 02/10/2000

FOR PRIOR APPLICATIONS VERIFIED:
JAPAN 11-263574 02/7/1999

Priority claimed ☐ yes ☒ no
35 USC 119 conditions met ☐ yes ☒ no
Inventor and Attorney/Agent/Examiner's initials

ATTORNEY DOCKET NO
001701.00000

TITLE: Synchronizing circuit for generating a signal synchronizing with a clock signal

U.S. DEPT. OF COM. / PAT. & TM-PTO-430L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims	Print Claim for O.G.	
ISSUE FEE			DRAWING		
Amount Due	Date Paid	Primary Examiner	Sheets Drwg.	Fig. Drwg.	Print Fig.
			Applicant Examiner		
<input type="checkbox"/> TERMINAL DISCLAIMER			PREPARED FOR ISSUE		
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